

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

Claim 1 (Currently Amended): A method for patterning a layer of a low dielectric constant material, comprising:

applying a surface imaging material having silicon incorporated therein on a layer of a low dielectric constant material, the surface imaging material having a thickness in the range from about 500 angstroms to about 2,500 angstroms;

defining a pattern in the surface imaging material;

hardening the patterned surface imaging material so that the patterned surface imaging material functions as a hard mask; and

transferring the pattern defined in the surface imaging material to the layer of the low dielectric constant material,

wherein the surface imaging material has silicon incorporated therein is incorporated with silicon without a separate silylation operation, and the hardening of the patterned surface imaging material includes exposing the patterned surface imaging material to an oxygen containing plasma.

Claim 2 (original): The method of claim 1, wherein the low dielectric constant material is selected from the group consisting of doped oxides, organic materials, and nanoporous materials.

Claim 3 (original): The method of claim 1, wherein the low dielectric constant material is a low dielectric constant polymeric material having a dielectric constant below about 3.0.

Claims 4 and 5 (canceled).

Claim 6 (Currently Amended): A method for forming an integrated circuit, comprising:

forming a layer of a low dielectric constant material on a substrate;

applying a surface imaging material having silicon incorporated therein on the layer of the low dielectric constant material, the surface imaging material having a thickness in the range from about 500 angstroms to about 2,500 angstroms;

defining a pattern in the surface imaging material;

hardening the patterned surface imaging material so that the patterned surface imaging material functions as a hard mask; and

transferring the pattern defined in the surface imaging material to the layer of the low dielectric constant material,

wherein the surface imaging material ~~has silicon incorporated therein~~ is incorporated with silicon without a separate silylation operation, and the hardening of the patterned surface imaging material includes exposing the patterned surface imaging material to an oxygen containing plasma.

Claim 7 (original): The method of claim 6, wherein the layer of low dielectric constant material has a thickness in the range from about 3,000 angstroms to about 10,000 angstroms.

Claim 8 (original): The method of claim 6, wherein the substrate is a layer of metal coated with a diffusion barrier.

Claim 9 (original): The method of claim 8, wherein the pattern transferred to the layer of the low dielectric constant material defines a via therein, and the method further comprises:

forming a layer of photoresist material over the hardened surface imaging material;
defining a trench pattern in the layer of photoresist material; and
transferring the trench pattern into a portion of the layer of the low dielectric constant material.

Claim 10 (original): The method of claim 8, wherein the pattern transferred to the layer of the low dielectric constant material defines a trench in an upper portion of the layer of the low dielectric constant material, and the method further comprises:

forming a layer of photoresist material over the hardened surface imaging material and the trench defined in the low dielectric constant material;
defining a via pattern in the layer of photoresist material; and
transferring the via pattern into a lower portion of the layer of the low dielectric constant material.

Claim 11 (original): The method of claim 8, wherein the layer of the low dielectric constant material forms a first dielectric layer, the pattern transferred to the first dielectric layer defines a via therein, and the method further comprises:

forming a second dielectric layer over the surface imaging material applied on the first dielectric layer, the second dielectric layer being formed of a low dielectric constant material;
applying the surface imaging material on the second dielectric layer;
defining a trench pattern in the surface imaging material applied on the second dielectric layer;

hardening the patterned surface imaging material applied on the second dielectric layer so that the patterned surface imaging material applied on the second dielectric layer functions as a hardmask; and

transferring the trench pattern defined in the surface imaging material applied on the second dielectric layer to the second dielectric layer in the same process used to transfer the via pattern to the first dielectric layer.

Claim 12 (original): The method of claim 6, wherein the low dielectric constant material is selected from the group consisting of doped oxides, organic materials, and nanoporous materials.

Claim 13 (original): The method of claim 6, wherein the low dielectric constant material is a low dielectric constant polymeric material having a dielectric constant below about 3.0.

Claims 14 and 15 (canceled).

Claim 16 (Currently Amended): A method for patterning a layer of a low dielectric constant polymeric material, comprising:

applying a surface imaging material having silicon incorporated therein on a layer of a low dielectric constant polymeric material, the surface imaging material having a thickness in the range from about 500 angstroms to about 2,500 angstroms;

defining a pattern in the surface imaging material;

hardening the patterned surface imaging material so that the patterned surface imaging material functions as a hard mask; and

transferring the pattern defined in the surface imaging material to the layer of the low dielectric constant polymeric material,

wherein the surface imaging material ~~has silicon incorporated therein is incorporated with silicon without a separate silylation operation~~, and the hardening of the patterned surface imaging material includes exposing the patterned surface imaging material to an oxygen containing plasma.

Claim 17 (original): The method of claim 16, wherein the low dielectric constant polymeric material has a dielectric constant below about 3.0.

Claims 18 and 19 (canceled).

Claim 20 (Currently Amended): A method for patterning a layer of a low dielectric constant material, comprising:

applying a surface imaging material having silicon incorporated therein on a layer of a low dielectric constant material;

defining a pattern in the surface imaging material;

hardening the patterned surface imaging material so that the patterned surface imaging material functions as a hard mask; and

transferring the pattern defined in the surface imaging material to the layer of the low dielectric constant material,

wherein the surface imaging material ~~has silicon incorporated therein is incorporated with silicon without a separate silylation operation~~, and the hardening of the patterned surface imaging material includes exposing the patterned surface imaging material to an oxygen containing plasma.

Claim 21 (original): The method of claim 20, wherein the low dielectric constant material is selected from the group consisting of doped oxides, organic materials, and nanoporous materials.

Claim 22 (original): The method of claim 20, wherein the low dielectric constant material is a low dielectric constant polymeric material having a dielectric constant below about 3.0.

Claims 23 and 24 (canceled).

Claim 25 (New): A method for forming an integrated circuit, comprising:

 forming a layer of a low dielectric constant material on a substrate with a diffusion barrier over a layer of metal;

 applying a surface imaging material on the layer of the low dielectric constant material;

 defining a pattern in the surface imaging material;

 hardening the patterned surface imaging material so that the patterned surface imaging material functions as a hard mask, wherein the hardening of the patterned surface imaging material includes exposing the patterned surface imaging material to an oxygen containing plasma;

 transferring the pattern defined in the surface imaging material to the layer of the low dielectric constant material to define a via therein;

 forming a layer of photoresist material over the hardened surface imaging material;

 defining a trench pattern in the layer of photoresist material; and

transferring the trench pattern into a portion of the layer of the low dielectric constant material.

Claim 26 (New): A method for forming an integrated circuit, comprising:

forming a layer of a low dielectric constant material on a substrate with a diffusion barrier over a layer of metal;

applying a surface imaging material on the layer of the low dielectric constant material;

defining a pattern in the surface imaging material;

hardening the patterned surface imaging material so that the patterned surface imaging material functions as a hard mask, wherein the hardening of the patterned surface imaging material includes exposing the patterned surface imaging material to an oxygen containing plasma;

transferring the pattern defined in the surface imaging material to the layer of the low dielectric constant material to define a trench in an upper portion of the layer of the low dielectric constant material;

forming a layer of photoresist material over the hardened surface imaging material and the trench defined in the low dielectric constant material;

defining a via pattern in the layer of photoresist material; and

transferring the via pattern into a lower portion of the layer of the low dielectric constant material.